

CS315-01 Project01 RISC V Assembly

Extra OH today 4:30pm to 6:30pm
in my office and zoom

Lab 01 good results

Project01

sign up sheet

20 mins

be prepared

ssh into curyalevm

ssh -T git@github.com

start local vm

ssh into local

ssh -T git@github.com

add alias

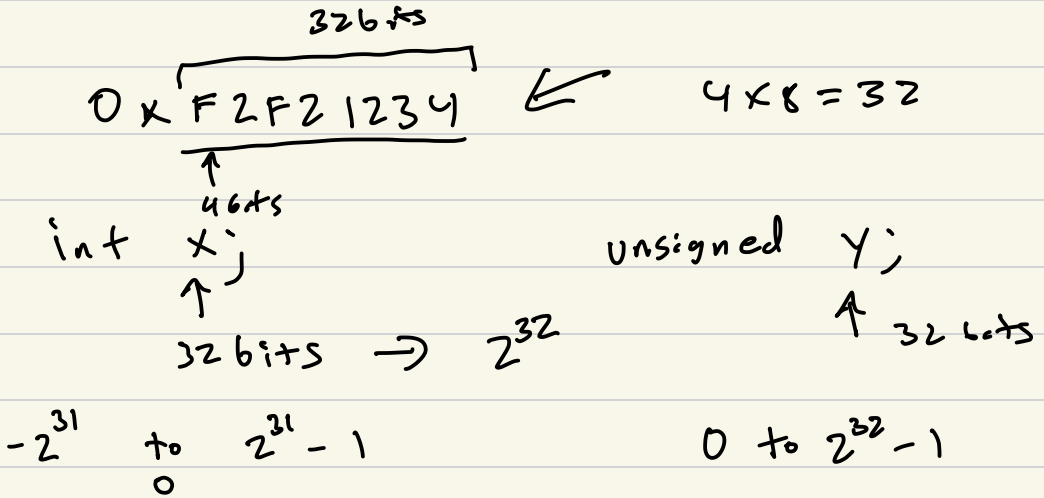
git done project01

make

autograder

Micro or VM to show code

Q: A signed vs unsigned



first arg base

"0b" → base = 2

"0x" → base = 16

otherwise → base = 10

string-to-int(char *s, int base)

RISC-V Assembly

Vocabulary

Mnemonics

registers

Variable
of the
processor

instructions

add, li, ret

32

x0 - x31

n0, a1

s0, s1, s2

args

Immediates

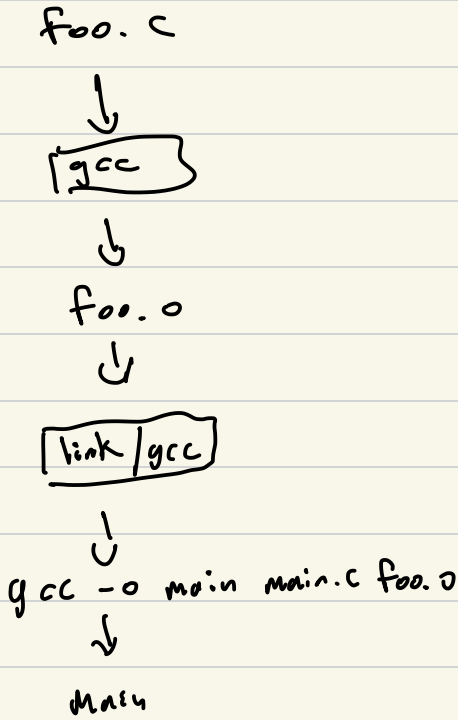
li s0, 2

Labels

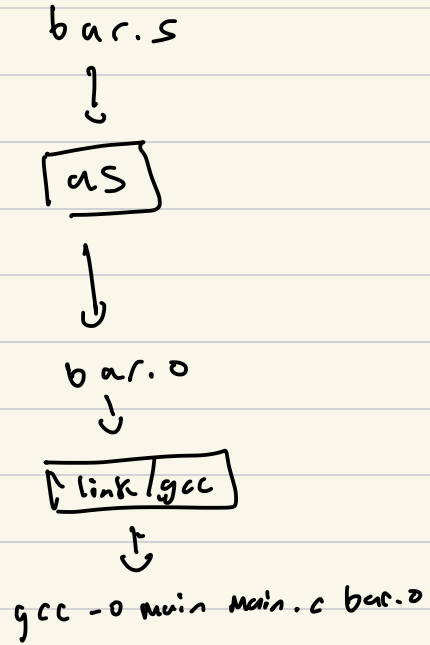
Foo:

add

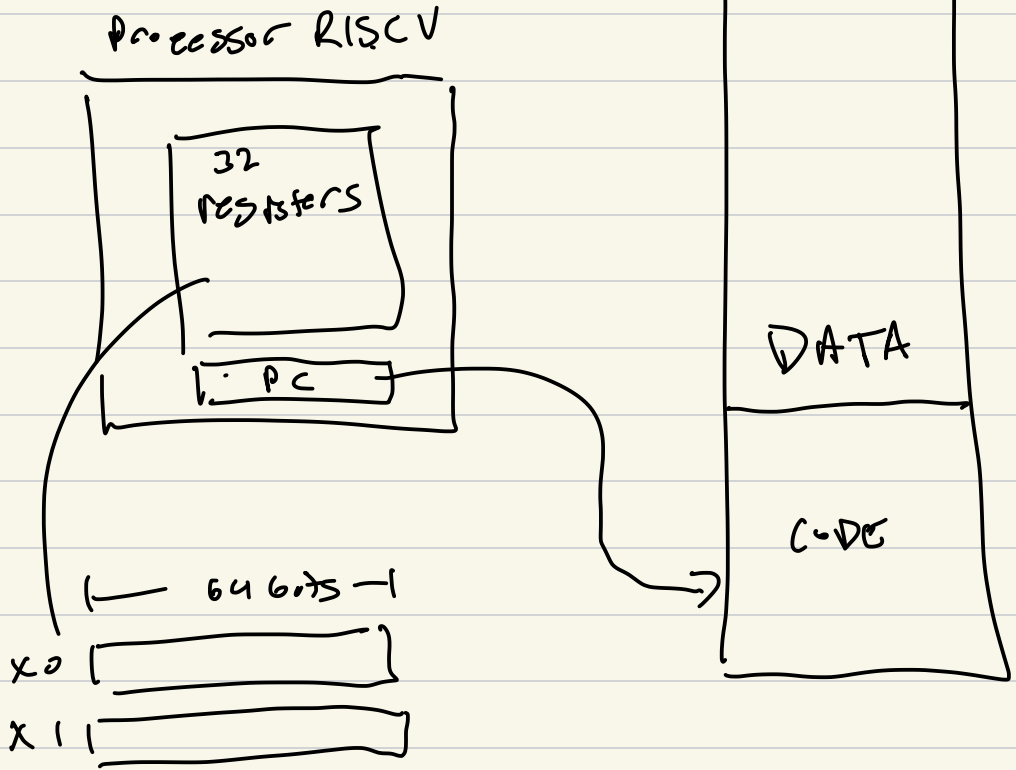
Compiling



Assembling



Programming Model



Instructions

- 1) Data processing
add, sub, mul
- 2) Control
- 3) Memory

